

REMARKS

In the outstanding Official Action, it was recognized that Applicant's prior arguments were persuasive, and the previous rejections were withdrawn. However, claims 1, 8-10, 12 and 14 were rejected under 35 USC 102(e) as anticipated by Thompson and claims 2-7, 13 and 15-17 were rejected under 35 USC 103(a) as being unpatentable over Thompson, a new and previously uncited reference, for the reasons of record.

In response, independent claims 1 and 12 are herewith amended in order to more clearly and precisely distinguish the instant invention over the cited and applied reference, and it is respectfully submitted that independent claims 1 and 12, and the remaining claims depending therefrom, are now clearly patentably distinguishable over the newly-cited art, for the reasons detailed below.

With regard to independent claims 1 and 12, it was suggested in the Action that column 2, lines 52-64 of Thompson a bias control circuit to provide desired dynamic controlling of amplification class parameters of the transistors. More particularly, however, it is respectfully submitted that the cited portion of Thompson in fact teaches a somewhat different circuit, namely one which compensates for "manufacturing process changes and temperature or

power supply voltage environmental variations" while maintaining a constant amplification class of each amplifiers stage, namely Class AB and Class C. As noted in the instant specification, Doherty amplifiers may have circuits that operate in different combinations of classes, so that controlling amplification class parameters to set the class of a particular amplifiers is not the same thing as compensating a fixed class amplifier for process, temperature or power supply voltage variations.


Additionally, independent claims 1 and 12 have been amended to add the additional limitation that both the first output line and the second output line comprise artificial transmission lines connected to the output terminal. It is respectfully submitted that these additional limitations are neither shown nor suggested in Thompson which on the contrary shows the output terminal connected to a separate output network 114, and each of the transistors having a simple electrical connection to this output network.

This additional feature, now more particularly and precisely recited in the independent claims, afford substantial commercial advantages. Using artificial transmissions lines results in a very compact structure, and permits very low impedance values to be obtained with a high quality factor and perfect repeatability in an easily implemented structure, with additional harmonics suppression

and better amplifier linearity. These advantages are detailed, *inter alia*, on page 4, paragraph 2 of the instant specification.

In view of the foregoing, it is respectfully submitted that independent claims 1 and 12, as herein amended, and the remaining claims depending therefrom are clearly patentably distinguishable over the cited and applied reference. Accordingly, allowance of the pending claims is respectfully submitted to be justified, and favorable consideration is earnestly solicited.

Respectfully submitted,

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